



2814
#5A Jno#
M. Brunson
PATENT 10/23/02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

DAVID W. WU
MICHAEL P. DUANE
SCOTT D. LUNING

Serial No.: 09/784,629

Filed: March 5, 1999

For: SEMICONDUCTOR DEVICE AND
METHOD FOR LOWERING MILLER
CAPACITANCE FOR HIGH-SPEED
MICROCAPACITORS

Examiner: P. Cao

Group Art Unit: 2814

Att'y Docket: 2000.032100/TT3633

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TECHNOLOGY CENTER 2800

RESPONSE TO OFFICE ACTION DATED DECEMBER 12, 2001

CERTIFICATE OF MAILING 37 C.F.R. 1.8	
I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on the date below:	
September 30, 2002 Date	 Signature

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

This paper is submitted in response to the Office Action dated July 31, 2002, for which the three-month date for response is October 31, 2002.

It is believed that a fee of \$84.00 is due. A check in that amount is enclosed for payment of this fee. Should any additional fees under 37 C.F.R. §§ 1.16 to 1.21 be required for any reason, the Assistant Commissioner is authorized to deduct said fees from Advanced Micro Devices, Inc. Deposit Account No. 01-365/TT3633.

The PTO did not receive the following
listed item(s) Fee \$84.00